

50, 40 & 30 W Programmable Constant Current LED Driver with Tri-Mode Dimming[™] (TRIAC, ELV & 0-10 V)

Nominal Input Voltage	Max. Output Power	Efficiency	Max. Case Temperature	THD	Power Factor	Dimming Method		Dimming Range	Startup Time	
120 to 277 Vac, 220 to 240 Vac	50 W	up to 90% typical	90°C (measured at the hot spot)	< 20% (from 100% to 50% of max rated power	`	Forward Reverse & 0 -	,	1 - 100% (% of lout)	200 ms typical	
	IAC/ELV Dimmer	Deb	+ LEDS	DV mer	Part Number	Nominal Input Voltage (Vac)	Max Output Power (W)	lout (mA)	Out Volt Range Min.	age (Vdc)
(\bigcirc)		PSB5			120 to 27			PUT VOLTA		max
\checkmark		Series	5 🛛 🖊	17	DOD001W 0700 40		B30W	050 + 200	00	40
					PSB30W-0700-42 PSB30W-1050-27	120 to 277 120 to 277	29.4 28.4	350 to 700 525 to 1050	28 18	42 27
			- LEDs		PSB30W-1050-27 PSB30W-0800-34	120 to 277		400 to 800	23	34
							B40W	100 10 000	20	01
					PSB40W-1400-27	120 to 277		700 to 1400	18	27
					PSB50W					
					PSB50W-0550-85	120 to 277	46.8	275 to 550	57	85
			_		PSB50W-0850-56	120 to 277	47.6	425 to 850	38	56
		Aluminun			PSB50W-1200-42	120 to 277	50.4	600 to 1200	28	42
2			°H 19.05 mm		PSB50W-1400-34	120 to 277	47.6	700 to 1400	23	34
	(L 3.8	8 * W 1.00) * H 0.75 in.)		220 to 240			PUT VOLTA	λE	
						-	B30W			
OTHER MOUNTING OPTIONS					PSB30W-0700-42-T	220 to 240	29.4	350 to 700	28	42
-	_		<u> </u>		PSB30W-1050-27-T	220 to 240	28.4	525 to 1050	18	27
					PSB30W-0800-34-T	220 to 240	27.2 B40W	400 to 800	23	34
	10				PSB40W-1400-27-T	220 to 240		700 to 1400	18	27
13-1-0			15		F3D40W-1400-27-1		B50W	700 10 1400	10	21
					PSB50W-0550-85-T	220 to 240	46.8	275 to 550	57	85
		0.			PSB50W-0850-56-T	220 to 240	47.6	425 to 850	38	56
			S.		PSB50W-1200-42-T	220 to 240	50.4	600 to 1200	28	42
Bottom Leads	with Stud	s Europe	ean Terminal Blo	cks	PSB50W-1400-34-T	220 to 240		700 to 1400	23	34
 2.5 kV ring v category A 	0%, 1.5 supply hours (case ho 4 as opti n: -5: 2 kV vave: AN	V to 0.6V= 2 Tc = 75° t spot tem on) case v line to line NSI/IEEE c 7 STAR®, I	1%, <0.6V=dii C operature vith silicone-ba 2 kV line to ea 62.41.1-2002 & DLC (DesignLig	m-to-off) ased potting arth & c62.41.2-2002 ght Consortium®)	 Data lo operati 	leads terminal b ad exit wit programm :: 100% to g read: S on, FW re nts (short ammable on-linear v r without of	locks h studs ming o 50% i KU, S/N ev., fault or surg and se with dim dim-to-o	n each volt I, lot code, events: po e), thermal lectable 0- ⁻ n-to-off, Log	hours wer fa 10V di	of ailure mmi
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